

**ED Poland**  
**by Zygmunt Ciota**

On June 26<sup>th</sup>, 2010, Wrocław, Poland took place a joint meeting of Poland Section of IEEE ED Chapter, and Section of Microelectronics from Committee of Electronics and Telecommunication of Polish Academy of Science. During the meeting the project of national design center of micro- and nano-systems based on Cadence facilities was discussed. Invited colleagues from Lisbon presented possibility of a similar center existing already in Portugal. The meeting was connected with distinguished lecture of Prof. Werner Weber from Infineon Technologies, Germany. The lecture was entitled “Energy Saving by Power Electronics in Household and Car Applications”.



*A discussion during the meeting*