

ED Poland
by Mariusz Orlikowski

On June 16th, 2011, Gliwice, Poland took place a joint meeting of Poland Section of IEEE ED Chapter and Section of Microelectronics of Committee of Electronics and Telecommunication of Polish Academy of Science. The meeting started with the presentation of Prof. Edward Hryniewicz from Silesian University of Technology entitled “*Logic Function Decomposition in Reed-Muller Spectral Domain*”. Taking the opportunity the areas of interests and research of Institute of Electronics (Silesian University of Technology) has been also presented.



Prof. E. Hryniewicz presentation