

ED Polish Chapter- by Mariusz Orlikowski

Polish ED Chapter is co-organizing the 20th International Conference MIXDES 2013 “Mixed Design of Integrated Circuits and Systems”, which will be held on 20-22 of June 2013 in Gdynia, Poland. For the opening the following presentations are planned:

- **“A Mixed-Design Technique for Integrated MEMS using a Circuit Simulator with HDL”**
- Prof. Hiroshi Toshiyoshi (The University of Tokyo, Japan)
- **“Design of ESD Resistant ICs and ESD Simulation”** - Prof. Valery Axelrad (SEQUOIA Design Systems, Inc, USA)
- **“The Art of Modeling and Predictive Simulation in Power Electronics and Microsystems”**
- Prof. Gerhard Wachutka (Technische Universität München, Germany)
- **“On the Use of Compact Modeling for RF/Analog Design Automation”** - Prof. Maria Helena Fino (Universidade Nova de Lisboa, Portugal)

During the conference two special sessions are planned:

- **“Compact Modeling for More than Moore”** organised by Dr. Daniel Tomaszewski (Inst. of Electron Techn., Poland) and Dr. Władysław Grabiński (GMC Suisse, Switzerland)
- **“xTCA for Instrumentation”** organised by Dr. Dariusz Makowski (Lodz University of Technology, Poland) and Dr. Stefan Simrock (ITER, France)

The papers for the conference may be submitted till **February 28th, 2013**. More information about the conference can be found at the web site <http://www.mixdes.org>.

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